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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Alan Welsh Sinclair  
Title: Adaptive Metablocks  
Application No.: 10/749,189 Filing Date: December 30, 2003  
Examiner: Unknown Group Art Unit: 2186  
Docket No.: SNDK.337US0 Conf. No.: 9883

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Mary E. Buggie  
Mary E. Buggie

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant calls the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patents and U.S. Published Patent Applications documents are not required and are therefore not enclosed. Copies of the listed foreign patent documents and Other Art are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a

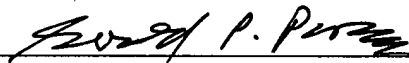
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representation that a search has been made (other than as indicated by any cited document), or  
(3) an admission that the cited information is, or is considered to be, material to patentability as  
defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and  
consequently no fee should be required. The Commissioner is authorized, however, to charge  
any fee that may be required, or to credit any overpayment, against Deposit Account No.  
502664. This form is being submitted in duplicate.

Respectfully submitted,



Gerald P. Parsons  
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October 20, 2005  
Date

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U.S. Department of Commerce, Patent and Trademark		Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		SN DK.337US0	10/749,189
		Applicant	Conf. No.
(Use several sheets if necessary)		Alan Welsh Sinclair	9883
(Form PTO-1449)		Filing Date	Art Group
OCT 24 2005		December 30, 2003	2186

U.S. Patent Documents							
Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1	5,367,484	11/1994	Alexander et al.			
	2	5,751,634	5/1998	Itoh			
	3	5,768,192	6/1998	Eitan			
	4	5,903,495	5/1999	Takeuchi et al.			
	5	6,011,725	1/2000	Eitan			
	6	6,141,249	10/2000	Estakhri et al.			
	7	6,377,500	4/2002	Fujimoto et al.			
	8	6,567,307	5/2003	Estakhri			
	9	6,763,424	7/2004	Conley			

U.S. Published Patent Application Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	10	2003/0053334A1	3/2003	Chen			
	11	2003/0110343A1	6/2003	Hagiwara et al.			
	12	2005/0144360A1	6/2005	Bennet et al.			
	13	2005/0141313A1	6/2005	Gorobets et al.			
	14	2005/0141312A1	6/2005	Sinclair et al.			
	15	2005/0166087A1	7/2005	Gorobets			
	16	2005/0144365A1	6/2005	Gorobets et al.			
	17	2005/0144363A1	6/2005	Sinclair			
	18	2005/0144358A1	6/2005	Conley et al.			
	19	2005/0144367A1	6/2005	Sinclair			

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	20	EP1424631A	6/2004	Europe			X	
	21	JP5314019	11/1993	Japan			Abstract	X
	22	WO 2004/040457	5/2004	WIPO			X	
	23	WO 2004/040458	5/2004	WIPO			X	
	24	WO 2004/040459	5/2004	WIPO			X	

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<b>Foreign Patent Documents (continued)</b>								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	25	WO 2004/040578	5/2004	WIPO			X	
	26	WO 00/049488	8/2000	WIPO			X	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
	27	Eitan et al., "NROM: A Novel Localized Trapping, 2-Bit Nonvolatile Memory Cell," IEE Electron Device Letters, vol. 21, no. 1, November 2000, pp. 543-545.						
	28	EPO/ISA, "Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration", mailed in related PCT/US2004/043692 on May 4, 2005, 9 pages.						
	29	ISA/EPO, "Communication Relating to the Results of the Partial International Search," mailed in related PCT/US2004/043680 on July 5, 2005, 3 pages.						
	30	Bennett et al., "Scratch Pad Block", U.S. Application Serial No. 11/016,285, filed 12/16/2004, 55 pages.						
	31	Chang, Li-Pin et al., "An Adaptive Striping Architecture for Flash Memory Storage Systems of Embedded Systems", Proceedings of the Eighth IEEE Real-Time and Embedded Technology and Applications Symposium, September 24, 2002, pp. 187-196.						
	32	EPO/ISA, "Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration", mailed in related PCT/US2004/043597 on August 8, 2005, 12 pages.						
	33	EPO/ISA, "Notification of Transmittal of the International Search Report and the Written Opinion of the International Searching Authority, or the Declaration", mailed in related PCT/US2004/043680 on September 23, 2005, 18 pages.						
Examiner			Date Considered					
<p><b>*EXAMINER:</b> Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>								